



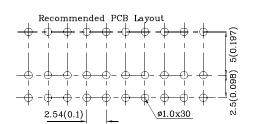
 $10 \; {
m SEGMENT} \; {
m BAR} \; {
m GRAPH} \; {
m ARRAY}$

Features

- Robust package
- ullet Uniform light disbursement
- Ideal for backlighting logos or icons
- Excellent for flush mounting
- Standard configuration: Gray face w/ white segments
- RoHS compliant







Package Schematics 25.4(1.0) 2.54(0. 24.64(0.97) + + + 2.5(0.098) 5(0.197) 10.16(0.4)5.08(0.2) В6 В8 B2 B5 B1 вз 1.78(0.07) ANODE MARK 8(0.315) $4(0.157)\pm0.5$ Ø0.5(0.02) + 0.25 2.54(0.1) B1 B2 B3 B4 B5 B6 B7 B8 B9 B10 Green Yellow

Notes

1. All dimensions are in millimeters (inches), Tolerance is $\pm 0.25 (0.01")$ unless otherwise noted.

2. Specifications are subject to change without notice.

Absolute Maximum Ratings (T _A =25°C)		Green (GaP)	Yellow (GaAsP/ GaP)	Unit	
Reverse Voltage	$V_{\rm R}$	5	5	V	
Forward Current	I_{F}	25	30	mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width	ifs	140	140	mA	
Power Dissipation	P_D	62.5	75	mW	
Operating Temperature	T_{A}	-40 ~	°C		
Storage Temperature	Tstg	-40 ~			
Lead Solder Temperature [2mm Below Package Base]	260°C For 3~5 Seconds				

A Relative Humidity between 40% and 60% is recommended in ESD-protected work areas to reduce static build up during assembly process (Reference JEDEC/JESD625-A and JEDEC/J-STD-033)

Operating Characteristics (T _A =25°C)		Green (GaP)	Yellow (GaAsP/ GaP)	Unit
Forward Voltage (Typ.) (I _F =10mA)	V_{F}	2	1.95	V
Forward Voltage (Max.)(I _F =10mA)	V_{F}	2.5	2.5	V
Reverse Current (Max.) (V _R =5V)	I_{R}	10	10	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I _F =10mA)	λΡ	565*	590*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) (I _F =10mA)	λD	568*	588*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I _F =10mA)	Δλ	30	35	nm
Capacitance (Typ.) (V _F =0V, f=1MHz)	С	15	20	pF

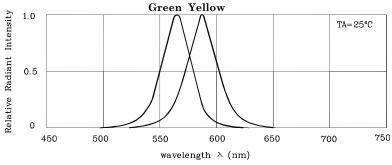
Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* (IF=10mA) ucd		Wavelength CIE127-2007* nm λP	Description
			min.	typ.		
XGUGUYX10D ——	Green	GaP	5600 1400*	11990 3990*	565*	10 Segments
	Yellow	GaAsP/GaP	2200 900*	8990 2390*	590*	Bar graph-Display

^{*}Luminous intensity value and wavelength are in accordance with CIE127-2007 standards.

Oct 18,2016

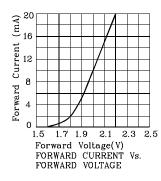


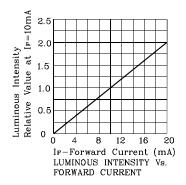


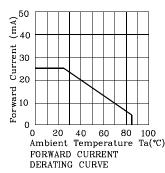


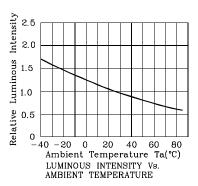
RELATIVE INTENSITY Vs. CIE WAVELENGTH

Green

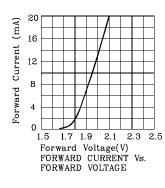


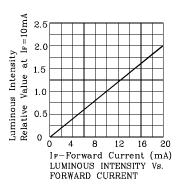


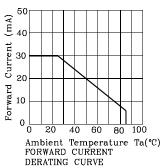


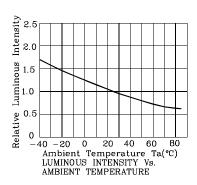


♦ Yellow

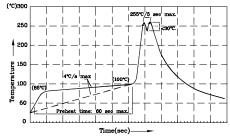








Wave Soldering Profile for Thru-Hole Products (Pb-Free Components)



ore—heat temperature of 105°C or less (as measured attached to the LED pins) prior to immersion in the maximum solder bath temperature of 256°C oldering temperature between 245°C ~ 255°C for 3 se

not apply stress to the epoxy resin while the temperature is above 85°C.
tures should not incur stress on the component when mounting and

Adving soldering process

SAC 305 solder alloy is recommended.

6.No more than one wave soldering pass.

7.During wave soldering, the PCS top-surface temperature should be kept below 105°C.

Remarks:

If special sorting is required (e.g. binning based on forward voltage,

luminous intensity / luminous flux, or wavelength),

the typical accuracy of the sorting process is as follows:

1. Wavelength: +/-1nm

2. Luminous Intensity / Luminous Flux: +/-15%

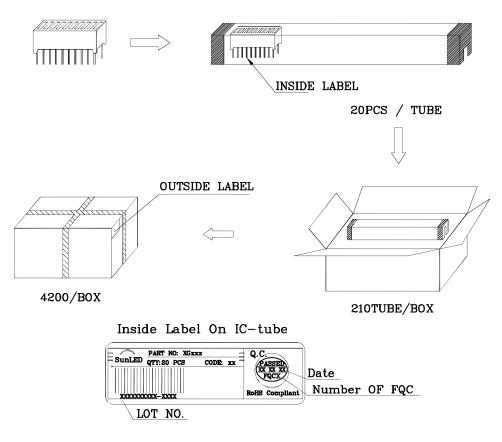
3. Forward Voltage: +/-0.1V

Note: Accuracy may depend on the sorting parameters.

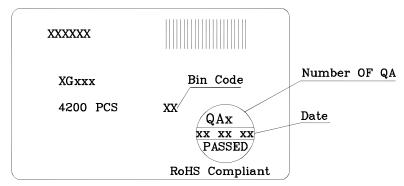




PACKING & LABEL SPECIFICATIONS



Outside Label On Box



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